1. **Catalog Description of the Course.** [Include the course prefix, number, full title, and units. Provide a course narrative including prerequisites and corequisites. If any of the following apply, include in the description: Repeatability (May be repeated to a maximum of ___ units); time distribution (Lecture ___ hours, laboratory ___ hours); non-traditional grading system (Graded CR/NC, ABC/NC). Follow accepted catalog format.]

**COMP 262. COMPUTER ORGANIZATION AND ARCHITECTURE (3)**

Three hours of lecture per week.
Prerequisite: COMP 151, COMP 162.
Extension of basic addressing concepts to more advanced addressability such as base register and self relative addressing. Topics include: comparative computer architecture focusing on such organizations as multiple register processors and stack machines; basics of virtual memory, input/output; introduction to the concept of microprogrammable systems; low-level language translation process associated with assemblers; system functions such as relocatable loading and memory management; application of data structure and hashing techniques to the above.

2. **Mode of Instruction.**

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<tr>
<th></th>
<th>Units</th>
<th>Hours per Unit</th>
<th>Benchmark Enrollment</th>
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<tbody>
<tr>
<td>Lecture</td>
<td>3</td>
<td>1</td>
<td>24</td>
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<tr>
<td>Seminar</td>
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<td>Laboratory</td>
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<tr>
<td>Activity</td>
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3. **Justification and Learning Objectives for the Course.** (Indicate whether required or elective, and whether it meets University Writing, and/or Language requirements) [Use as much space as necessary]

The course is a required course for Computer Science majors according to accreditation guidelines.

Through this course, students will be able to

- Evaluate the architecture of a computer system
- Compare and contrast computer architectures
- Design circuits at the gate-level using and/or/not/multiplexors/decoders/encoders
- Design microcode sequences to implement machine level instructions
- Organize and express ideas clearly and convincingly in oral and written forms.

This course is not designed to satisfy the University Writing or Language requirements.

4. **Is this a General Education Course**

<table>
<thead>
<tr>
<th>If Yes, indicate GE category:</th>
<th>YES</th>
<th>NO</th>
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<tbody>
<tr>
<td>A (English Language, Communication, Critical Thinking)</td>
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<td>B (Mathematics &amp; Sciences)</td>
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<td>C (Fine Arts, Literature, Languages &amp; Cultures)</td>
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<td>D (Social Perspectives)</td>
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<td>E (Human Psychological and Physiological Perspectives)</td>
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5. **Course Content in Outline Form.** [Be as brief as possible, but use as much space as necessary]

NEWCRSFR 9/30/02
Multi-level machines, interpretation vs translation
Processor overview, RISC/CISC
Pipelining and other parallelism.
Memory: byte-ordering, error detection/correction, cache, secondary memory.
Gates and Boolean algebra
Microarchitecture, microinstructions, implementation of macroarchitecture
Instruction level architecture
Virtual memory and I/O: Paging, segmentation, virtual I/O, files
Assembly language level: macros, linking and loading

6. References. [Provide 3 - 5 references on which this course is based and/or support it.]

7. List Faculty Qualified to Teach This Course.
   All Computer Science faculty

8. Frequency.
   a. Projected semesters to be offered: Fall ___X__ Spring _X____ Summer ___X__

9. New Resources Required.
   a. Computer (data processing), audio visual, broadcasting needs, other equipment
      Use of existing computer lab.

   b. Library needs
      none

   c. Facility/space needs
      none

10. Consultation.
    Attach consultation sheet from all program areas, Library, and others (if necessary)

11. If this new course will alter any degree, credential, certificate, or minor in your program, attach a program modification.

Proposer of Course Date

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